

Giuseppe Iannaccone

Professore Ordinario presso il [Dipartimento di Ingegneria dell'Informazione](#)
Settore scientifico disciplinare: Elettronica ING-INF/01

Recapito

e-mail: giuseppe.iannaccone@unipi.it

sito web: <http://www.iannaccone.org>

indirizzo della sede ufficiale: VIA G. CARUSO, 16 56122 PISA

Curriculum

Updated 23 Apr 2019 - always updated information on <http://www.iannaccone.org>

Birth date: 1968

Citizenship: Italian

Work Address: Dipartimento di Ingegneria dell'Informazione, Università di Pisa,
Via G. Caruso 16, I-56122 Pisa, Italy, phone: +39 050 2217677,
mobile +39 366 6709149, e-mail: giuseppe.iannaccone@unipi.it ,

Employment

From Dec 2012: Professor of Electrical Engineering at the University of Pisa

January 2001 – Nov 2012: Associate Professor of Electrical Engineering at the University of Pisa.

May 1996 – December 2000: Assistant Professor of Electrical Engineering at the University of Pisa.

January 1996 - May 1996: Permanent position as Research Scientist Italian National Research Council

Education

- 1996: Ph.D. in Electrical Engineering from the University of Pisa with a thesis on "Transport and noise phenomena in quantum effect devices"
- 1992: "Laurea" summa cum laude in Electrical Engineering (MSEE) from the University of Pisa with a thesis on "Transport phenomena and modeling of resonant tunneling diodes"

Awards

- * 2015 - Fellow of the American Physical Society
- * 2014 - Fellow of the Institute of Electronics and Electrical Engineers
- * 1993 - Mariponave Sabre (Marina Militare Italiana)

Research:

- Modeling of nanoelectronic devices for logic and memory.
- Study of noise in nanoelectronic devices and circuits
- Development of tools for Technology CAD.
- Investigation of scaling and variability in integrated circuits.
- Design of nanopower electronic circuits for RFID transponders, implantable systems.
- Design of systems for internet of things applications

Bibliometrics:

- 200+ papers in peer-reviewed journals, and 130+ paper in proceedings of International Conferences.
- 4900+ citations according to ISI Web of Science (h factor 31)
- 5900+ citations according to Scopus (h factor 34).
- 12500+ citations according to Google scholar (h factor 43)
- 6000+ citations according to ResearchGate (h factor 36)
- 30+ invited presentations at international conferences and invited lectures at international schools.

Teaching:

- 2001 to present: Electronics (12 Credits) for the BSEE curriculum
- 2011 to present: Power electronics (6 credits) for MSEE curriculum
- 2015 to present: Electronic Systems (3 credit module) for the MS curriculum in Computer Engineering
- 2004 to 2010: Electronic Infrastructures (6 Credits) for MSEE curriculum
- 2002 and 2003: Electrical Measurements for the MSEE curriculum
- 2010 short course on RFIDs and the Internet of Things for PhD students
- 1996-2001: Lecture series of Analog Electronics, Semiconductor Technologies, Optoelectronics.

Project Management

- * Coordinator of European Project NANOTCAD (FP5) 1999-2003
- * Coordinator of European Project DEWINT (ESF) 2006-2009
- * Coordinator of European Project QUEFORMAL (h2020) 2019-2021
- * Coordinator of PRIN-MIUR Project FIVE2D 2019-2022
- * PI of several European and National coordinated project 2000-2015

Services and Technology Transfer:

- President of the Italian Inter-University Consortium on Nanoelectronics IU.NET
- Founder of Quantavis s.r.l., as a spinoff of the FP5 NanoTCAD project (2007).
- President of the University of Pisa Committee for Patents and Technology Transfer (2015-2016, Member 2006-2016)